

CLAIMS

1. Method for updating software present in a first version (V1) in a device (40),
5 by receiving and executing a delta file (D12) for upgrading said first version to a second version (V2), which software is divided and stored in a number of memory blocks (201-204) defined in an address space (211) of a physical memory (48) in the device, comprising the steps of:
 - defining an extra memory block (212) associated with said number of memory
10 blocks;
 - erasing said extra memory block to define an extra memory block space;
 - writing updated data for a first of said number of memory blocks, as determined by said delta file, on said extra memory block;
 - erasing said first memory block, thereby moving said extra memory block space
15 one block; and
 - writing updated data for a second of said number of memory blocks, as determined by said delta file, on said first memory block.
2. The method as recited in claim 1, wherein said extra memory block is defined
20 as a memory block placed adjacent to said first memory block in said address space.
3. The method as recited in claim 1, comprising the step of processing all of said number of memory blocks one by one by moving said extra memory block space one block at a time; and writing data on that of said number of memory blocks on
25 which an extra memory block space is defined, from the next memory block in said address space, during upgrading as determined by said delta file, until all of said number of memory blocks have been shifted one step in the address space.
4. The method as recited in claim 1, comprising the step of reversing the order of
30 writing memory blocks in the address space, from said upgrade from a first version to a second version, to a second upgrade from said second version to a third version.

5. The method as recited in claim 4, comprising the steps of:

- said device, before receiving and executing a delta file, connecting with a server and communicating information related to a current address space status

- 5 determining which type of delta file is applicable for upgrading said software, dependent on the present location of said extra block in said address space, wherein a first delta file type is adapted to upgrade the software present in said memory blocks in one order in the address space, and a second delta file adapted to upgrade the software present in said memory blocks in a reversed order in the address space;
- 10 - downloading a delta file of the applicable delta file type from said server to said device; and
- upgrading said software using the applicable delta file.

6. The method as recited in any of the previous claims, comprising the step of

- 15 modifying a start address within said address space in a boot code for said software, dependent on the moving of data between said memory blocks during writing as determined by said delta file.

7. Computer program product, for use in a computer-controlled electronic device

- 20 (40) for updating software present in a first version (V1) in the device, which device comprises means for receiving and executing a delta file (D12) for upgrading said first version to a second version (V2), which software is divided and stored in a number of memory blocks (201-204) defined in an address space (211) of a physical memory (48) in the device, said computer program product comprising executable
- 25 computer program code devised to cause the device to perform the steps of:
- defining an extra memory block with said number of memory blocks;
- erasing said extra memory block to define an extra memory block space;
- writing updated data for a first of said number of memory blocks, as determined by said delta file, on said extra memory block;
- 30 - erasing said first memory block, thereby moving said extra memory block space one block; and

- writing updated data for a second of said number of memory blocks, as determined by said delta file, on said first memory block.

8. The computer program product as recited in claim 7, comprising executable
5 computer program code devised to cause the device to perform the steps of any of the previous claims 1-6.

9. Radio communication terminal (40) comprising a computer system (47) with associated computer code for updating software present in a first version (V1) in the
10 terminal, and means (46) for receiving and executing a delta file (D12) for upgrading said first version to a second version (V2), which software is divided and stored in a number of memory blocks (201-204) defined in an address space (211) of a physical memory (48) in the terminal, **characterised in** that an extra memory block (212) associated with said number of memory blocks is defined in said
15 address space, said terminal further comprising means for erasing said extra memory block for defining an extra memory block space; data writing means for writing updated data for a first of said number of memory blocks, as determined by said delta file, on said extra memory block; data erasing means for erasing said first memory block for moving said extra memory block space one block; wherein said
20 data writing means are devised to write updated data for a second of said number of memory blocks, as determined by said delta file, on said first memory block.

10. The radio communication terminal as recited in claim 9, wherein said extra memory block is placed adjacent to said first memory block in said address space.
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11. The radio communication terminal as recited in claim 9, wherein said data writing means are devised to process all of said number of memory blocks one by one from said extra memory block by writing data on one of said number of memory blocks from the next memory block in said address space, during
30 upgrading as determined by said delta file, until all of said number of memory blocks have been shifted one step in the address space.

12. The radio communication terminal as recited in claim 9, comprising means for reversing the order of writing memory blocks in the address space, from said upgrade from a first version to a second version, to a second upgrade from said
5 second version to a third version.

13. The radio communication terminal as recited in claim 12, wherein said device is arranged to connect with a server before receiving and executing a delta file, for communicating information related to a current address space status determining
10 which type of delta file is applicable for upgrading said software, dependent on the present location of said extra block in said address space, wherein a first delta file type is adapted to upgrade the software present in said memory blocks in one order in the address space, and a second delta file adapted to upgrade the software present in said memory blocks in a reversed order in the address space, said terminal
15 comprising means for downloading a delta file of the applicable delta file type from said server to said device, and means for upgrading said software using the applicable delta file.

14. The radio communication terminal as recited in any of the previous claims 9-
20 13, comprising means for modifying a start address within said address space in a boot code for said software, dependent on the moving of data between said memory blocks during writing as determined by said delta file.